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		BONI, ANDREA; "1.2-Gb/s True PECL 100K Compatible I/O Interface in 0.35-um CMOS," IEEE Journal of Solid State Circuits, Vol. 36, No. 6, June 2001, pages 979-987, IEEE, 445 Hoes Lane, Piscataway, NJ 08854-1331,USA	
		DJAHANSHAHI, HORMOZ; "Gigabit-per-Second, ECL-Compatible I/O Interface in 0.35-um CMOS," IEEE Journal of Solid State Circuits, Vol. 34, No. 8, August 1999, pages 1074-1083, IEEE, 445 Hoes Lane, Piscataway, NJ 08854-1331, USA	
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